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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/696,409	10/28/2003	Zvi Regev	4528	
7590 01/13/2006			EXAMINER	
ZVI REGEV 24217 HIGHLANDER RD. WEST HILLS, CA 91307			YOUNG, BRIAN K	
			ART UNIT	PAPER NUMBER
			2819	· ·
			DATE MAILED: 01/13/2006	5

Please find below and/or attached an Office communication concerning this application or proceeding.

		Application No.	Applicant(s)			
Office Action Summary		10/696,409	REGEV, ZVI			
		Examiner	Art Unit			
		Brian Young	2819			
The MAILING DATE of this communication appears on the cover sheet with the correspondence address Period for Reply						
WHIC - Exter after - If NC - Failu Any	ORTENED STATUTORY PERIOD FOR REPLY CHEVER IS LONGER, FROM THE MAILING DANS INSTRUCTION OF THE	ATE OF THIS COMMUNICATION 36(a). In no event, however, may a reply be tim vill apply and will expire SIX (6) MONTHS from a cause the application to become ABANDONE	N. nely filed the mailing date of this communication. D (35 U.S.C. § 133).			
Status						
1)⊠	Responsive to communication(s) filed on 17 No.	ovember 2005.				
	This action is FINAL . 2b) This action is non-final.					
3)	, _					
closed in accordance with the practice under Ex parte Quayle, 1935 C.D. 11, 453 O.G. 213.						
Dispositi	on of Claims					
5)⊠ 6)⊠ 7)⊠	Claim(s) <u>1-27</u> is/are pending in the application. 4a) Of the above claim(s) is/are withdraw Claim(s) <u>1-6</u> is/are allowed. Claim(s) <u>25-27</u> is/are rejected. Claim(s) <u>7-24</u> is/are objected to. Claim(s) are subject to restriction and/or	vn from consideration.				
Applicati	on Papers					
10)⊠	The specification is objected to by the Examiner The drawing(s) filed on <u>26 October 2005</u> is/are: Applicant may not request that any objection to the or Replacement drawing sheet(s) including the correction to the or declaration is objected to by the Example 1.	a)⊠ accepted or b)⊡ objected drawing(s) be held in abeyance. See on is required if the drawing(s) is obj	e 37 CFR 1.85(a). ected to. See 37 CFR 1.121(d).			
Priority u	ınder 35 U.S.C. § 119					
a)[Acknowledgment is made of a claim for foreign All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priori application from the International Bureau see the attached detailed Office action for a list of	s have been received. s have been received in Application ity documents have been receive (PCT Rule 17.2(a)).	on No ed in this National Stage			
Attachment	:(s)					
2) 🔲 Notice 3) 🔯 Inform	e of References Cited (PTO-892) e of Draftsperson's Patent Drawing Review (PTO-948) nation Disclosure Statement(s) (PTO-1449 or PTO/SB/08) r No(s)/Mail Date 10/28/03.	4) Interview Summary (Paper No(s)/Mail Da 5) Notice of Informal Pa 6) Other:				

Application/Control Number: 10/696,409 Page 2

Art Unit: 2819

1. Claims 7-24 are objected to because of the following informalities: 7,12,17 and 21 do not end with a period. Claim 5 recites "the output of the flip-flops or sampling devices" which has no antecedent basis in the claims. Appropriate correction is required.

2. The following is a quotation of the second paragraph of 35 U.S.C. 112:

The specification shall conclude with one or more claims particularly pointing out and distinctly claiming the subject matter which the applicant regards as his invention.

3. Claims 25-27 are rejected under 35 U.S.C. 112, second paragraph, as being indefinite for failing to particularly point out and distinctly claim the subject matter which applicant regards as the invention.

Claims 25-27 recite "an apparatus to determine the instantaneous phase of a complex signal" with <u>no</u> circuit elements recited. The elements, which comprise the apparatus, must be recited. There is no way to determine how the apparatus performs the operation.

4. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Art Unit: 2819

5. Claim 25 is rejected under 35 U.S.C. 102(b) as being anticipated by Birgenbeier et al.

Birgenbeier et al. disclose (fig.1) an apparatus to determine the instantaneous phase (14) of a complex signal (see I and Q) utilized in an instantaneous frequency (frequency error) measurement apparatus.

Birgenbeier et al. recites (col. 2, ln.14-18):

"Differentiation of the phase trajectory provides the **instantaneous frequency** of the signal from which the carrier frequency may be subtracted to provide the frequency deviation of the signal."

Birgenbeier et al. also recites (col. 2, ln.39-42):

"A linear regression analysis performed on the phase difference versus time measurement provides an estimate of the frequency error as well as the **instantaneous phase** error."

- 6. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 7. Claim 26 is rejected under 35 U.S.C. 103(a) as being unpatentable over Birgenbeier et al.

As noted above, Birgenbeier et al. disclose (fig.1) an apparatus to determine the instantaneous phase (14) of a complex signal (see I and Q).

It is noted that Birgenbeier et al. do not specifically disclose the apparatus utilized with "digital RF memories".

Page 4

However, Birgenbeier et al. do disclose that the circuitry may be used with a digitized RF signal (fig.1, RF Signal input and Digitizer, 1).

Therefore, it would have obvious to one of ordinary skill in the art, to utilize the instantaneous phase determining circuit of Birgenbeier et al. in a digital RF memory circuit, because, obviously RF signals are used in RF memory circuits and Birgenbeier et al. teaches a method for calculating the phase/ frequency of a digitized RF signal, which is useful when modulating/ error correction of RF signals such as for use in a digital memory circuit for accuracy and control of the memory signals.

8. Claim 27 is rejected under 35 U.S.C. 103(a) as being unpatentable over Birgenbeier et al.

As noted above Birgenbeier et al. disclose (fig.1) an apparatus to determine the instantaneous phase (14) of a complex signal (see I and Q).

It is noted that Birgenbeier et al. do not specifically recite that the apparatus may be used in a "signal restoration" circuit.

Application/Control Number: 10/696,409 Page 5

Art Unit: 2819

However, Birgenbeier et al. do disclose that the circuitry may be used to determine an "ideal" modulated signal. Analysis is performed using mathematical and linear regression to determine the ideal signal.

Birgenbeier et al. also recites (col. 2, ln.32-42):

Utilizing the data clock phase, the detected data sequence and the time interval of interest, a digital signal synthesizer mathematically generates the ideal phase trajectory corresponding in the transmitted signal. The ideal phase trajectory thus generated is subtracted from the previously measured phase trajectory of the transmitted signal to provide a signal phase difference versus time measurement. A linear regression analysis performed on the phase difference versus time measurement provides an estimate of the frequency error as well as the instantaneous phase error.

Therefore, it would have been obvious to one having ordinary skill in the art, to utilize the phase measuring apparatus of Birgenbeier et al. for signal restoration, because mathematical/ linear regression, combined with signal analysis, is a well know manner for computing/ restoring signals and accurate measurement of the phase is necessary to completely correct/ restore the signals.

- 9. Claims 1-6 are allowed.
- 10. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

Application/Control Number: 10/696,409 Page 6

Art Unit: 2819

Yamaguchi, et al disclose a clock signal that has been converted into a digital signal and transformed into a complex analytic signal, and an instantaneous phase is estimated.

11. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Brian Young whose telephone number is 571-272-1816. The examiner can normally be reached on Mon-Fri 7:30-4:00.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Rexford Barnie can be reached on 571-272-7492. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Primary Examiner

Art Unit 2819
